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Rad Hard Logic ICs Deliver High Performance And Low Cost For Small Satellites

<u>Apogee Semiconductor's</u> AP54RHC RadHard logic family is based on the company's Transistor-Adjusted-Layout for Radiation (TalRad) design methodology that improves the radiation performance of commercial process technologies, enabling the rapid creation of rad-hard designs in a fraction of the time and effort normally required to design rad hard ICs. Previewed at NSREC 2020 in December, the AP54RHC family of products is built with cold-sparing capabilities and triple-redundancy providing maximum reliability and area savings.

The family is tailored to small satellite applications with a total ionizing dose (TID) resilience of 30 krad (Si) and single event latch-up (SEL) hardened up to with 80 MeV-cm²/mg, encapsulated in a 14-pin TSSOP plastic package. Parts are designed for class 2 ESD and specified for operation over a -55°C to +125°C temperature range and a 1.65-V to 5.5-V V_{DD} range.

As with other products the company is developing, these new logic chips are meant to bridge the performance gap that exists between commercially available, state-of-the-art devices and lower-performance rad hard components. Meanwhile, they also aim to close the price gap between the very expensive rad hard parts and their commercial counterparts.

"Our products and services are targeted towards enabling small satellites and large constellations that require high performance, small form factors and radiation resilience at a lower cost," stated Anton Quiroz, CEO. "Apogee Semiconductor is aggressively focused on addressing the challenges small satellite spacecraft designers are facing to meet aggressive cost targets while also needing to maintain high reliability."

The AP54RHC family includes functions such as level-translators (such as a two supply level shifter), majority voters (with daisy chain error detect), transceivers and other 7400 series logic functions (see Figs. 1 and 2). Evaluation units are available now and flight units will be available in 2Q21. The company has also been sampling 300-krad versions of the AP54RHC devices. An announcement on these devices is expected soon.

Contact Apogee Semiconductor if you are interested in evaluating the AP54RHC products. For more information, see the <u>website</u> or email the <u>company</u>.

 1.65 VDC to 5.5 VDC operation First in the industry! 	Г	MCU 1 OUL 1 A1	oter 1	
 Provides logic-level down translation to VCC 		103 123 42	Vote output Y1	functions
 Extended operating temperature range (-55 °C to +125 °C) 	shared stimuli / inputs	MCU 2 Out 2 B1	control system indication	fault
 Proprietary cold-sparing capability with zero static power penalty 		MCU 3 OUT 3 C1	Eour1	notification
 Built-in triple redundancy 				
 Internal power-on reset (POR) 				
 Class 2 ESD protection 		B1 2	13 D EDET	
• (4000 V HBM, 500 V CDM)		C1 🖂 3	12 🗖 Eout2	
• TID resilience of 30 krad (Si)		Y1 □ 4 Eout1 □ 5	11 - Y2 10 - C2	
 SEL resilient up to LET of 80 MeV-cm²/mg 			9 🗖 B2	
 300krad (Si) version also available 		GND = 7	8 A 2	

Fig . 1. *The AP54RHC301 is a dual three-input majority voter.*



VccA VccY	
A1 +2	
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	
A4 5 9 10 Y4	
$\begin{array}{c} \mathbf{A1} \\ \mathbf{A2} \\ \mathbf{A2} \\ \mathbf{A3} \end{array}$	13 🗆 Y1 12 🗆 Y2
A3 🗖 4	11 🗖 Y3
	$ \begin{array}{c} VccA VccY \\ A1 $

Fig. 2. The AP54RHC504 is a five-channel level translator.