

ISSUE: February 2021

## Determining Design Power Over An Input Voltage Range (Part 2): Inductor Design Power

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In part 1, the maximum power handled by the inductor of a PWM-switch converter,  $\overline{P}_{L_{\text{max}}}$ , was defined in relation to input power  $\overline{P}_g$  for the three PWM-switch configurations (see the reference).  $\overline{P}_{L_{\text{max}}}$  tells us the maximum amount of power the inductor will carry over the  $V_g$  range, but this is not the power rating we can use to optimally design the inductor for size. To that end, we need a new parameter, which I have dubbed design power.

Why? Because the inductor has a fixed structure that must accommodate the ranges of both the voltage and the current it sees. The maximum values in each of these ranges,  $V_{gmax}$  and  $\bar{i}_{gmax}$ , must be considered. While

 $V_{gmax}$  and  $\overline{i}_{gmax}$  never occur together, the inductor must be designed for both. This suggests for design a maximum input power of

$$\overline{P}_{g\max} = V_{g\max} \cdot \overline{i}_{g\max}$$

where  $V_{gmax}$  is the maximum input voltage and  $\bar{\iota}_{gmax}$  is the maximum average input current (both at the input to the switch).

However, this  $\overline{P}_{gmax}$  is a worst-case (maximum) value for calculating the *inductor design power*,  $\overline{P}_{Ld}$ . The power-rating question needs further investigation because  $\overline{P}_{gmax}$  is not necessarily the optimal value of *inductor design power*. As we'll see, that will depend on the PWM-switch configuration.

## Design Power Vs. Input Power

We can relate  $\overline{P}_{g \max}$  to average inductor input power as follows. At minimum  $V_{g}$ ,

$$\overline{P}_{g} = V_{g\min} \cdot \overline{i}_{g\max} = (V_{g\max} / r) \cdot \overline{i}_{g\max} = \overline{P}_{g\max} / r$$

Or equivalently, at the maximum- $V_g$  operating-point,

$$\overline{P}_{g} = V_{g \max} \cdot \overline{i}_{g \min} = V_{g \max} \cdot (\overline{i}_{g \max} / r) = \overline{P}_{g \max} / r$$

At both extremes,

$$\overline{P}_{g\max} = r \cdot \overline{P}_{g} = \left(\frac{V_{g\max}}{V_{g\min}}\right) \cdot \overline{P}_{g}$$

The corresponding maximum on-time power amplitude is

$$P_{g\max} = V_{g\max} \cdot I_{g\max} = (r \cdot V_{g\min}) \cdot I_{g\max} = V_{g\max} \cdot (r \cdot I_{g\min})$$
$$= r \cdot P_g = \left(\frac{V_{g\max}}{V_{g\min}}\right) \cdot P_g$$



These power quantities are graphed in Fig. 1. Each pair of power curves on the same graph differ by a factor of *r*.



Fig. 1. Input-power plots at rated power,  $P_g$  and the greater design power,  $P_{gmax}$  (left graph). Similar plots for average  $P_g$  and average design  $P_g$  are shown on the right. The plot pairs are separated by the  $V_g$  (and  $I_g$ ) range of r.

The power curves within the operating region have maximum voltages,  $V_{gmax}$  that limit the inductor core flux, and also maximum current,  $I_{gmax}$  or  $\tilde{i}_{gmax}$  that limits the winding current. Inductor wire size must be large enough for  $\tilde{i}_{Lmax} \approx \tilde{i}_{Lmax}$  for a near-constant current waveform. ( $\tilde{i}_{Lmax}$  varies parametrically in "design space" with  $V_g$  but not in time as a waveform.) D controls  $\bar{v}_L$  (and hence flux) over the  $V_g$  range. Because of D,  $\bar{v}_L$  can be varied and inductor design power can be less than  $\bar{P}_{gmax}$  as

$$\overline{P}_{Ld} = \overline{i}_{L\max} \cdot \overline{v}_{L\max}$$

For the three PWM-switch configurations, inductor design power is defined as

$$\overline{P}_{Ld} = \overline{i}_{L\max} \cdot \overline{v}_{L\max} = \overline{i}_{g\max} \cdot (D \cdot V_g)_{\max} , \text{CA}$$
$$\overline{P}_{Ld} = \overline{i}_{L\max} \cdot \overline{v}_{L\max} = (\overline{i}_{g\max} / D_{\max}) \cdot (D' \cdot D \cdot V_g)_{\max} , \text{CH}$$
$$\overline{P}_{Ld} = \overline{i}_{L\max} \cdot \overline{v}_{L\max} = (\overline{i}_{g\max} / D_{\max}) \cdot (D \cdot V_g)_{\max} , \text{CL}$$

Then the design-power ratio,

$$\frac{\overline{P}_{Ld}}{\overline{P}_{g}} = \frac{\overline{i}_{L\max} \cdot \overline{v}_{L\max}}{\overline{i}_{g\max} \cdot V_{g\min}} = \frac{\overline{i}_{L\max}}{\overline{i}_{g\max}} \cdot \frac{\overline{v}_{L\max}}{V_{g\min}}$$

The CA and CL  $\bar{v}_{L\,\mathrm{max}}(V_g)$  is found from,

$$\frac{d\overline{v}_L}{dV_g} = \frac{d}{dV_g} (D \cdot V_g) = D + V_g \cdot \frac{dD}{dV_g} = 0$$

from which the values of  $V_g$  and D at which  $\bar{v}_L$  is maximum are used to find  $(D \cdot V_g)_{\text{max}}$ .



For the CA,

$$\frac{d}{dV_g}(D \cdot V_g) = D + V_g \cdot \frac{dD}{dV_g} = \left(1 - \frac{V_g}{V_o}\right) + V_g \cdot \left(-\frac{1}{V_o}\right) = 1 - 2 \cdot \frac{V_g}{V_o} = 0 \implies \frac{V_g}{V_o} = \frac{1}{2} \implies D(\bar{v}_{L\text{max}}) = 1 - \frac{1}{2} = \frac{1}{2}$$

Then the CA

$$\overline{v}_{L \max} = (D \cdot V_g)_{\max} = D(\overline{v}_{L \max}) \cdot V_g(D(\overline{v}_{L \max})) = \frac{1}{2} \cdot (V_o / 2) = V_o / 4$$

and  $\,\bar{i}_{L\,{\rm max}}=\bar{i}_{g\,{\rm max}}$  . Thus

$$\frac{\overline{P}_{Ld}}{\overline{P}_g} = \frac{1}{4} \cdot \frac{V_o}{V_{g \min}} , \text{CA}$$

For the CL,

$$\frac{d}{dV_g}(D \cdot V_g) = D + V_g \cdot \frac{dD}{dV_g} = \left(\frac{V_o}{V_o + V_g}\right) + V_g \cdot \left(-\frac{V_o}{(V_o + V_g)^2}\right) = \left(\frac{V_o}{V_o + V_g}\right)^2 = 0$$
$$\Rightarrow [D(\bar{v}_{Lmax})]^2 = 0 \Rightarrow D(\bar{v}_{Lmax}) = 0 \Rightarrow V_g \to \infty$$

That is, the function continues to increase with increasing  $V_g$ . It is maximum within the range at  $V_{gmax}$  and  $D_{min}$ ;

$$\overline{v}_{L \max} = (D \cdot V_g)_{\max} = D_{\min} \cdot V_g \max = \frac{V_o}{V_o + V_g \max} \cdot V_g \max \Longrightarrow$$
$$\frac{\overline{P}_{Ld}}{\overline{P}_g} = \frac{D_{\min}}{D_{\max}} \cdot \left(\frac{V_g \max}{V_g \min}\right) \implies \frac{\overline{P}_{Ld}}{\overline{P}_g} = \frac{V_o + V_g \min}{V_o + V_g \max} \cdot \left(\frac{V_g \max}{V_g \min}\right), \text{CL}$$

For the CP,

$$\frac{d\overline{v}_L}{dV_g} = \frac{d}{dV_g} (D' \cdot D \cdot V_g) = \frac{d}{dV_g} \left( \left( 1 - \frac{V_o}{V_g} \right) \cdot V_o \right) = \left( \frac{V_o}{V_g} \right)^2 = 0 \implies [D(\overline{v}_{L\text{max}})]^2 = 0 \implies D(\overline{v}_{L\text{max}}) = V_o/V_g = 0 \implies V_g \to \infty$$

As for the CL, the CP  $\bar{v}_L(V_g)$  monotonically increases over the  $V_g$  range to a maximum at  $V_{gmax}$  and  $D_{min}$ . Then

$$\bar{v}_{L\max} = (1 - D_{\min}) \cdot D_{\min} \cdot V_{g\max}$$

$$\frac{\overline{P}_{Ld}}{\overline{P}_g} = \frac{\overline{i}_{L\max}}{\overline{i}_{g\max}} \cdot \frac{\overline{v}_{L\max}}{V_{g\min}} = (1 - D_{\min}) \cdot \frac{D_{\min}}{D_{\max}} \cdot \frac{V_{g\max}}{V_{g\min}} \Longrightarrow$$



$$\frac{\overline{P}_{Ld}}{\overline{P}_g} = 1 - \frac{V_o}{V_{g \max}} , \text{ CP}$$

The design formulas are summarized in the table.

Table.	PWM-switch	inductor	design-power ratios.
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Config	D	$D(\overline{v}_{L\max})$	$\overline{v}_{L \max}$	$rac{\overline{P}_{Ld}}{\overline{P}_{g}}$	$\frac{\overline{P}_{Ld}}{\overline{P}_{L\max}}$
CA (boost)	$1 - \frac{V_g}{V_o}$	1⁄2	$\frac{V_{g \max}}{2}$	$\frac{1}{4} \cdot \left( \frac{V_o}{V_{g\min}} \right)$	$\frac{\left(V_{o} / 2\right)^{2}}{V_{g\min} \cdot \left(V_{o} - V_{g\min}\right)}$
CP (buck)	$rac{V_o}{V_g}$	$D_{\min}$	$V_{gmax}$	$1 - \frac{V_o}{V_{g \max}}$	1
CL (boost-buck)	$\frac{V_o}{V_o + V_g}$	$D_{\min}$	$V_{g\max}$	$\frac{V_o + V_{g\min}}{V_o + V_{g\max}} \cdot \left(\frac{V_{g\max}}{V_{g\min}}\right)$	$\frac{V_o + V_{g\min}}{V_o + V_{g\max}} \cdot \left(\frac{V_{g\max}}{V_{g\min}}\right)$

Plots of the design-power ratios as a function of  $V_g$  and  $V_o$  for the three configurations are shown in Fig. 2, with  $V_{gmin} = 10 \text{ V}$ ,  $V_{gmax} = 30 \text{ V}$ , and for the same midrange ( $V_g = 20 \text{ V}$ ) values of D = 0.333 as in the graph of  $D(V_g)$  in Fig. 1. The  $\overline{P}_{Ld}/\overline{P}_g$  are 0.75 at  $V_{gmax}/2$  (CA), 0.778 at  $V_{gmax}$  (CP), and 1.5 at  $V_{gmax}$  (CL). The CL configuration has the most power-intensive requirement for the inductor for which  $\overline{P}_{Ld}/\overline{P}_g$  is always greater than one.



Fig. 2. Plots of the design-power ratios (  $\overline{P}_{Ld} / \overline{P}_{g}$  ) for the three PWM-switch configurations.

How does the design-power ratio,  $\overline{P}_{Ld} / \overline{P}_g$  affect inductor size? One indicator of size is the required fraction of the total window area,  $A_w$  of the winding area,  $A_{ww} = N \cdot A_{cwp}$ , where  $A_{cwp}$  is the insulated wire conductor area,  $A_{cw}$ , expanded by the packing factor of the wire for each of N turns. Then  $A_{ww} \leq A_w$  and for a winding of full window occupancy  $A_{ww} = A_w$ . For maximum core transfer power, N is

$$N_{opt} = \sqrt{N_{\lambda} \cdot N_{i}} = \sqrt{\frac{V_{g} \cdot t_{on}}{\Delta \phi} \cdot \frac{N\bar{i}_{sat}}{\bar{i}_{g}}} = \text{constant} \cdot \sqrt{\frac{V_{g}}{\bar{i}_{g}}}$$



Conductor area,  $A_c$  at constant current density is proportional to  $\tilde{i}_g = \kappa_g \cdot \bar{i}_g$ . The minimum required window area is

$$A_{\scriptscriptstyle ww} = N \cdot A_{\scriptscriptstyle cwp} \propto \sqrt{\frac{V_g}{\bar{i}_g}} \cdot \bar{i}_g = \sqrt{V_g \cdot \bar{i}_g} = \sqrt{\overline{P_g}}$$

At worst,  $\overline{P}_{Ld} \leq \overline{P}_{g \max} = r \cdot \overline{P}_{g}$  replaces  $\overline{P}_{g}$  for design, and  $A_{_{WW}} \propto \sqrt{r} \cdot \sqrt{\overline{P}_{g}}$ . As the  $V_{g}$  range increases,  $A_{WW}$ 

increases by  $\sqrt{r}$ . This is the size and cost tradeoff for the benefit of a wide  $V_g$  range. It explains why converters with a fixed input voltage have a higher power density, and why the universal power-line voltage range has not become widely accepted for cost-driven offline converter designs.

In part 3, we take this same kind of formula derivation for design to transformers.

## Reference

"<u>Determining Design Power Over An Input Voltage Range (Part 1): Maximum Inductor Power</u>" by Dennis Feucht, How2Power Today, November 2020.

## **About The Author**



Dennis Feucht has been involved in power electronics for 40 years, designing motordrives and power converters. He has an instrument background from Tektronix, where he designed test and measurement equipment and did research in Tek Labs. He has lately been working on projects in theoretical magnetics and power converter research.

For more on magnetics design, see these How2Power Design Guide search <u>results</u>.