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3.3-V-Based BCD Platform Supports Development Of High-Performance PMICs

From [Tower Semiconductor](#), a foundry of high-value analog semiconductor solutions, PML is a 300-mm, 65-nm, 3.3-V-based BCD power management platform. It joins the company's 5-V-based offering already in high-volume production in Japan and that which is being qualified in the company's Albuquerque, New Mexico manufacturing site. This cutting-edge platform addresses the stringent low-voltage requirements of mobile devices and meets the growing demands for high power efficiency and power density in AI and data center applications.

The advanced 300-mm BCD PML offering comprises LDMOS devices with ultra-low on-resistance and best-in-class figure-of-merit, achieving highest power conversion efficiency for fast switching converters, according to the vendor. In addition, it features power devices with wide voltage range and a nominal 3.3-V gate voltage that can be substantially overdriven and underdriven addressing products such as PMICs, audio ICs, and high-power voltage regulators for GPU and CPU. These advantages enable users to achieve outstanding performance in power consumption and extend battery life in battery operated applications.

"Our new PML platform exemplifies Tower Semiconductor's continuous success in providing cutting-edge power management technology solutions," said Shimon Greenberg, general manager of the Power Management Business Unit. "Specifically designed for advanced power management applications, this innovation empowers our customers to develop industry-leading products with a competitive edge that address the evolving demands of the strategic mobile, AI, and data center markets".

For additional information on Tower's PM technology platform, see the Power Management [page](#).