PwrSoC Workshop Is Coming To Seoul In September

The International Workshop on Power Supply on Chip (PwrSoC) will be held this year from September 24-26 at Seoul National University in Seoul, Korea. PwrSoC is the world's leading forum dedicated to the advancement of miniaturized and integrated power conversion and power management technologies. Held biennially, the workshop gathers experts from academia, industry, and the global supply chain to discuss current challenges and explore future opportunities in the field.

Host Institution: Seoul National University

Founded in 1946, Seoul National University (SNU) is Korea's premier academic institution, widely recognized for its excellence in education and research. With a student body of approximately 30,000, SNU has been instrumental in driving Korea's scientific, technological, and economic growth. The university serves as an ideal venue for this workshop, offering a vibrant academic environment and world-class facilities.

Technical Program

The technical program follows a single-track format to encourage active participation and discussion. Each session features presentations from leading researchers and engineers in power supply integration.

The program is overseen by technical program chair Hyun-Sik Kim (KAIST, Korea) and vice chair Rinkle Jain (Nvidia, U.S.A), along with 22 internationally recognized experts. A complete and up-to-date technical schedule is available at Programs - (pwrsocevents.com).

Session title	Session chairs/TPC members
Integrated Magnetics	Kousuke Miyaji (Shinshu Univ., Japan) Cian O'Mathuna (Tyndall Institute, Ireland) Matt Wilkowski (Wurth Elektronik, USA)
System Integrated Packaging & Manufacturing	Sang Won Yoon (Seoul National Univ., Korea) Hongbin Yu (Arizona State Univ., USA)
Integrated Capacitors and Energy Storage	Jaeil Baek (KAIST, Korea) Mohamed Mehdi Jatlaoui (Murata, France) Raj Pulugurtha (Florida International Univ., USA)
Wide-Bandgap Integration	Bernhard Wicht (Leibniz Universität Hannover, Germany) Jayasimha Prasad (Infineon, USA) Wanyuan Qu (Zhejiang Univ., China)
Systems & Applications	Se-Un Shin (POSTECH, Korea) Raveesh Magod (Texas Instruments, USA) Francesco Carobolante (Intel, USA)

Granular Power Supply	Christopher Schaef (Microsoft, USA) Cheng Huang (Iowa State Univ., USA) Amit Jain (Intel, USA)
Topologies & Control	Sung-Wan Hong (Sogang Univ., Korea) Mauro Leoncini (Politecnico di Milano, Italy) Jason Stauth (Dartmouth College, USA)
Poster Session	Hyung-Min Lee (Korea Univ., Korea) Trifon Liakopoulos (EnaChip, USA)

Poster Session

The poster session provides an excellent opportunity to share innovative research and foster direct interaction.

- Abstract submission deadline: July 18, 2025

- Best Poster awards will be presented to three outstanding submissions.

For submission guidelines, visit the workshop's Posters page.

Industry Visit: SK Hynix Fab Tour

As part of the program, participants will be offered a guided tour of SK Hynix's advanced semiconductor fabrication facility. This exclusive visit provides an inside look into one of the world's top memory manufacturers and its state-of-the-art processes and technologies.

Registration And Additional Information

Registration will open soon. We invite professionals, researchers, and students in the field of power electronics and system integration to participate in this high-impact international event.

To register or learn more, visit the workshop website.